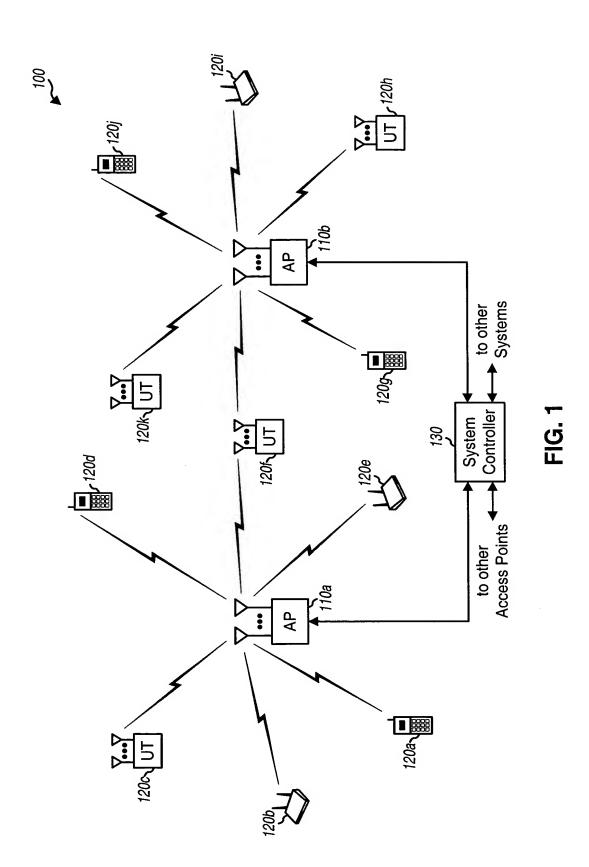
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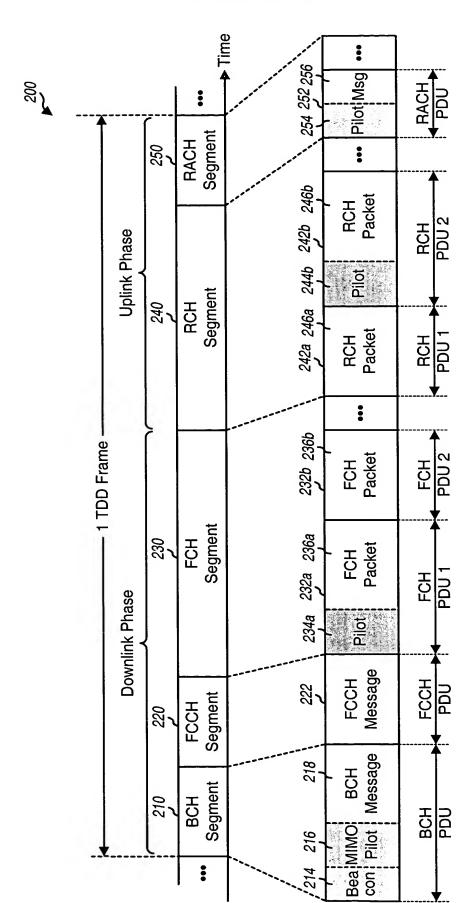


FIG. 2

Sheet 3/13 Data Source Memory Source **★►** Memory 388y 382y Data Sink Data Sink 388 7 Data 372y 382x 372x Processor 390x User Terminal (UT) Controller Processor Controller Processor TX Data **RX** Data TX Data Processol **RX** Data 370y 380y 390y 370x 380x **∑** F (S) Processor Channel Estimator TX Spatial Channel Estimator RX Spatial Processor Processor RX Spatial 360y 360x 378y 392y 378x 354ut User Terminal (UT) 354a DEMOD DEMOD MOD DEMOD MOD MOD 352ut 352x 352a 120/S 120x DEMOD DEMOD 324ap ' MOD MOD 322ap 322a] Fap(K) FIG. 3 RX Spatial Estimator Channel Processor $\overline{\mathbf{M}}_{\mathrm{ap}}(\mathbf{K})$ 340 Processor TX Spatial 328 Access Point (AP) 350 110X 330 Processor 342 Processor **RX** Data TX Data 310 Scheduler Memory Source Data Sink 5 Data 308 -332 344 334

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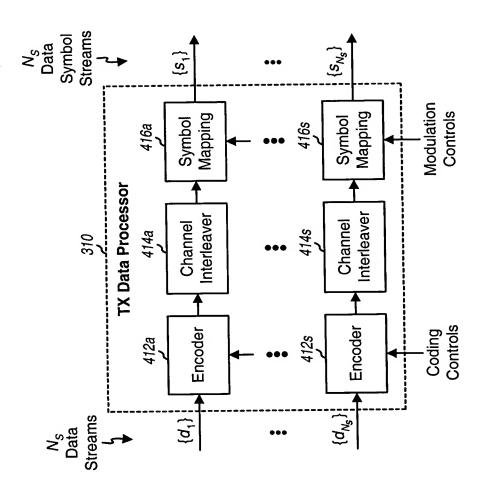
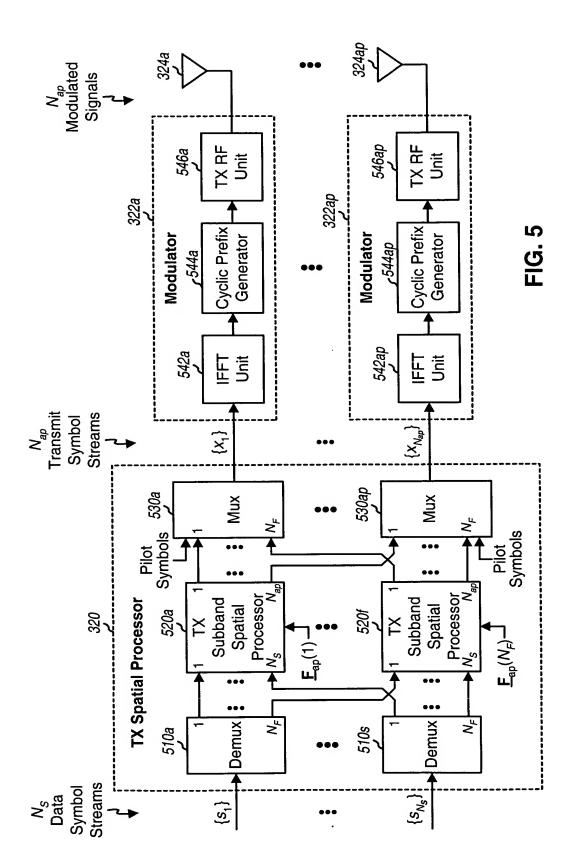


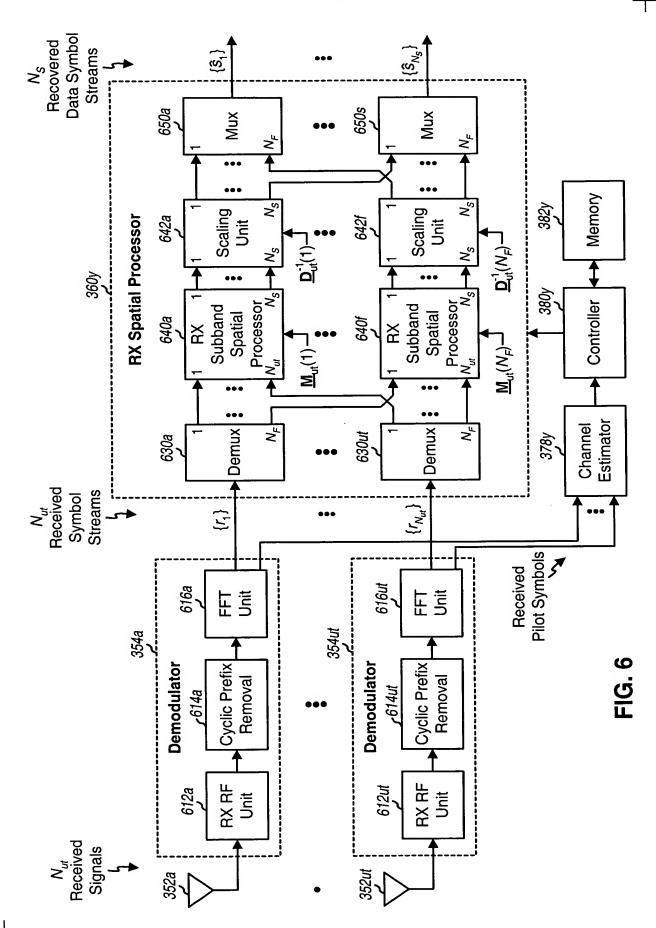
FIG. 4

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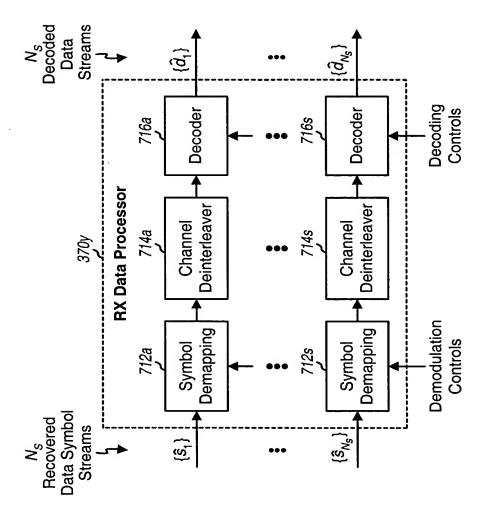


FIG. 7

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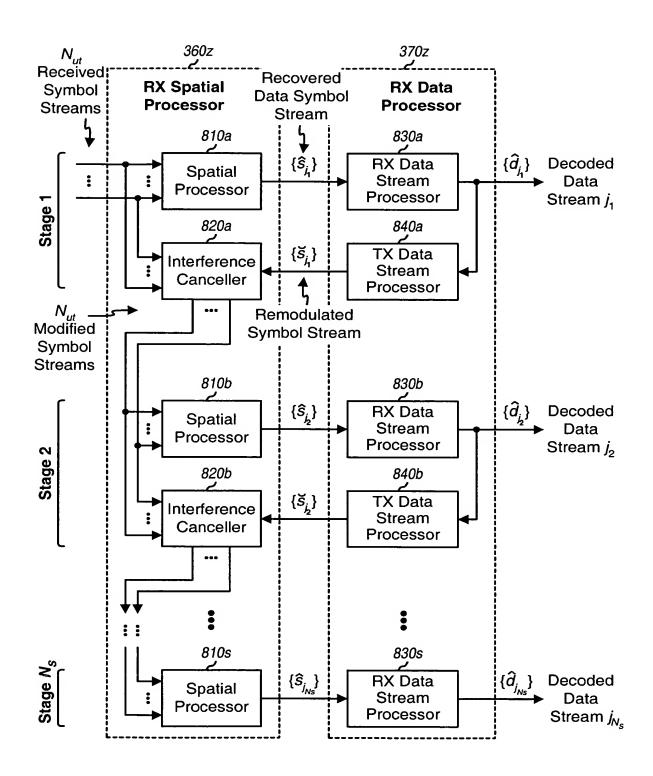


FIG. 8

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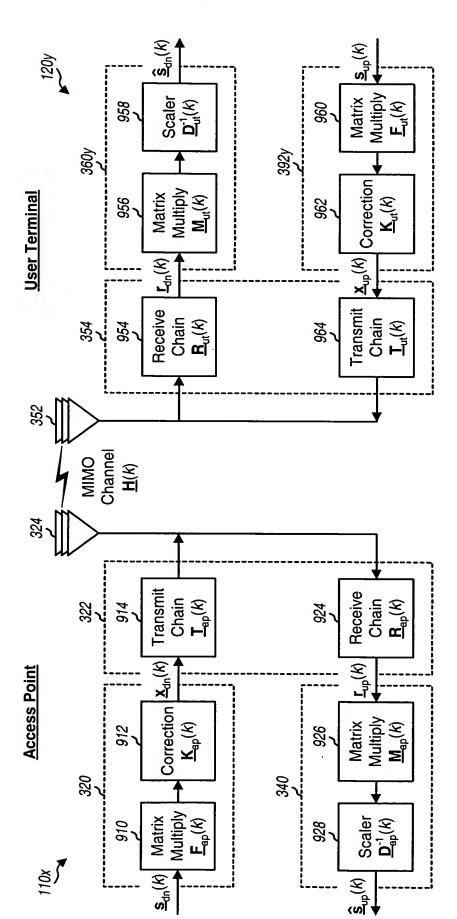
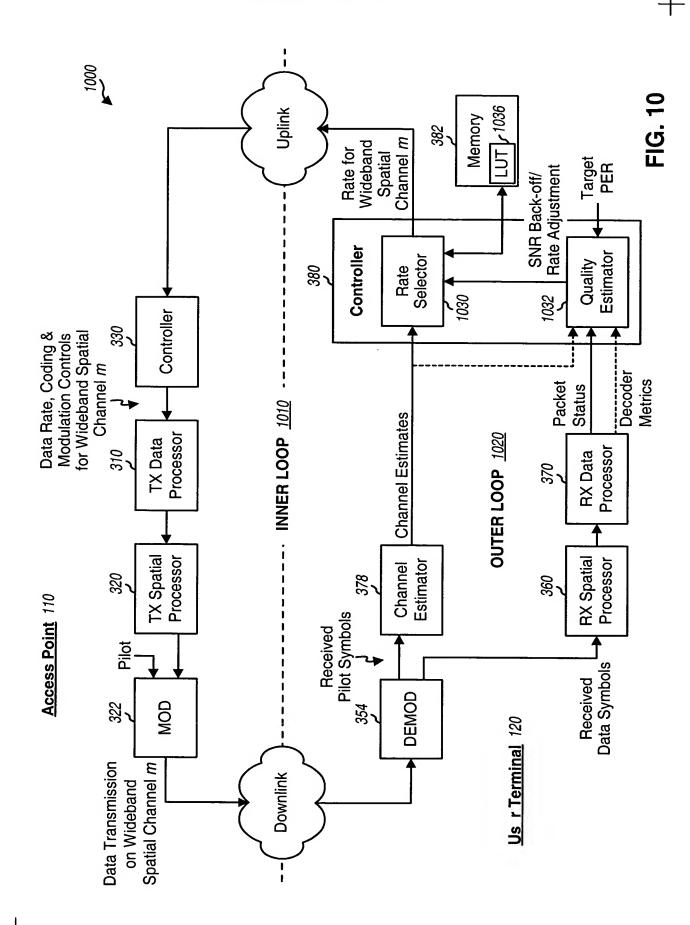


FIG. 9

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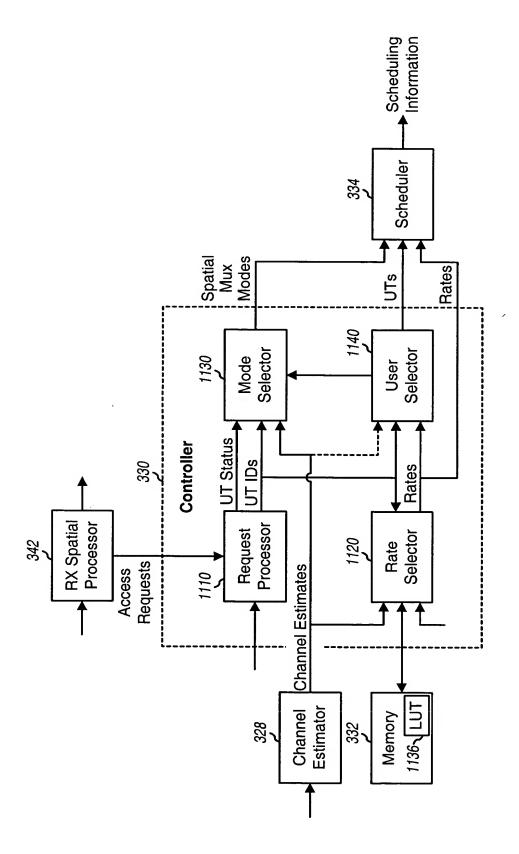


FIG. 11

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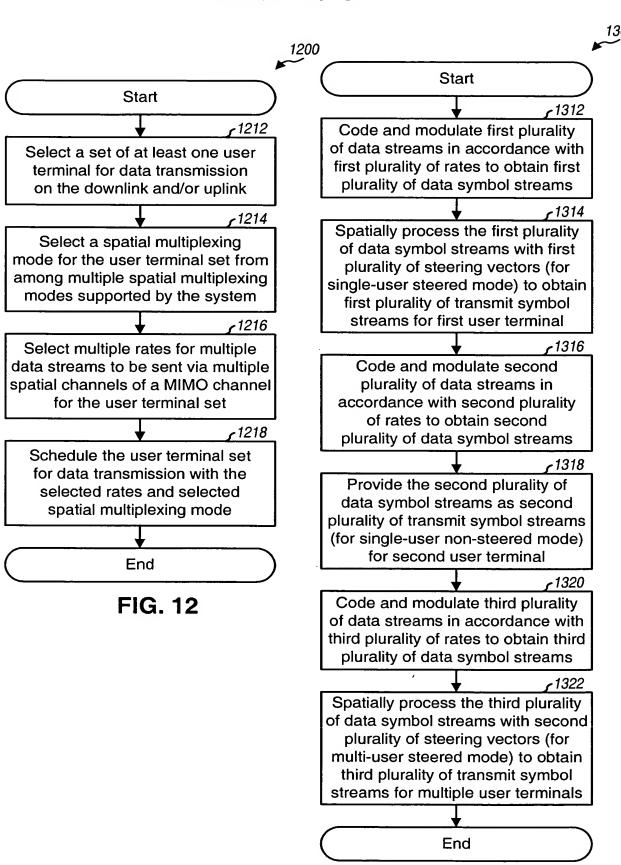


FIG. 13

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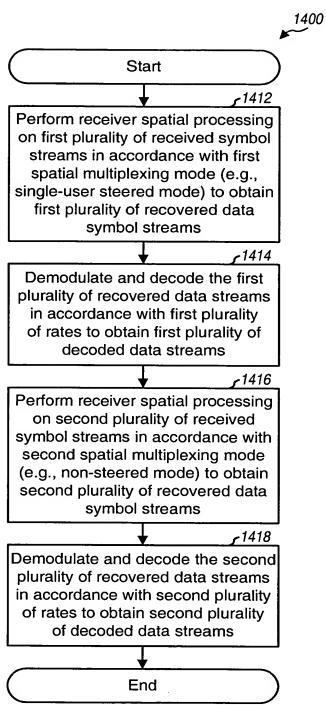


FIG. 14